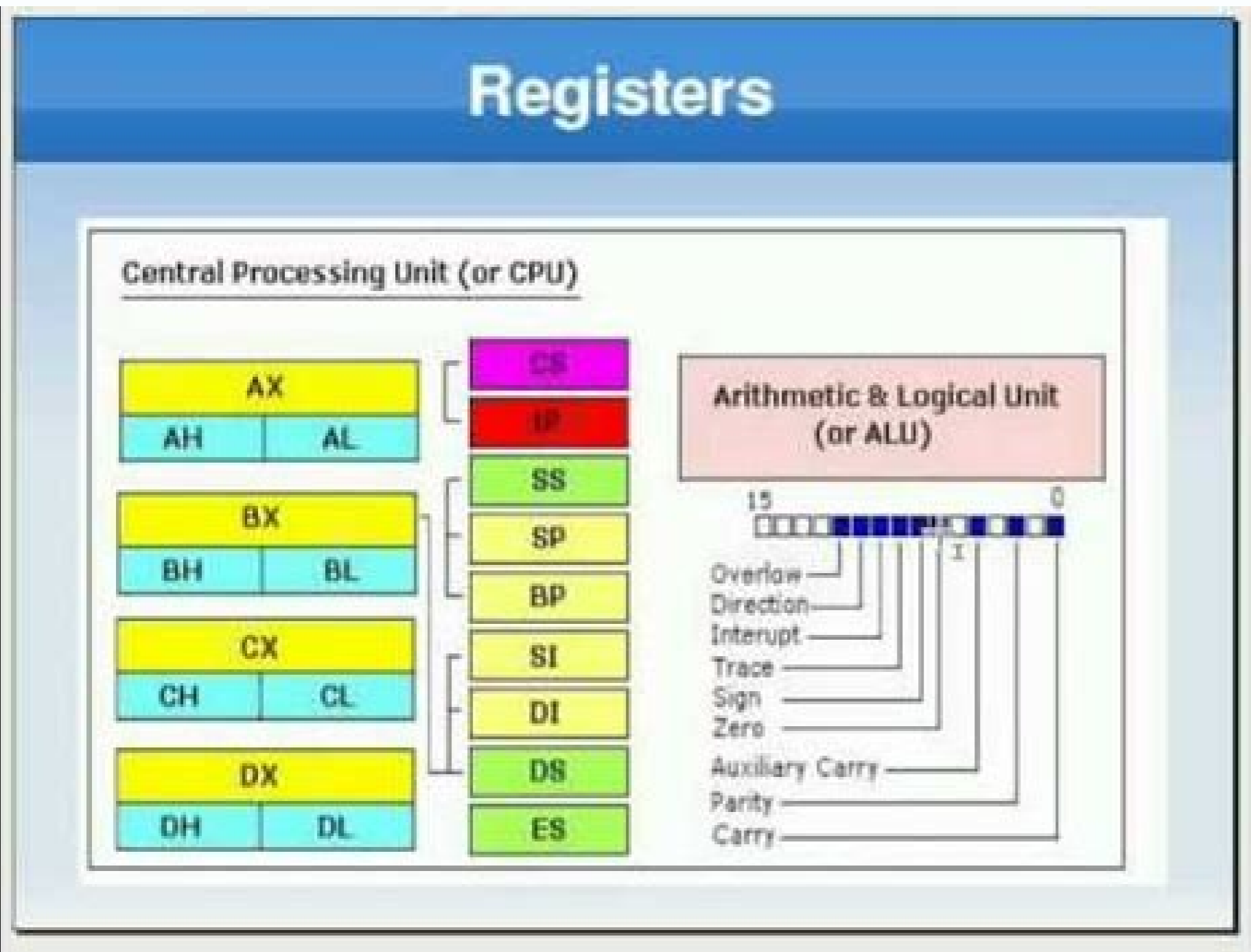
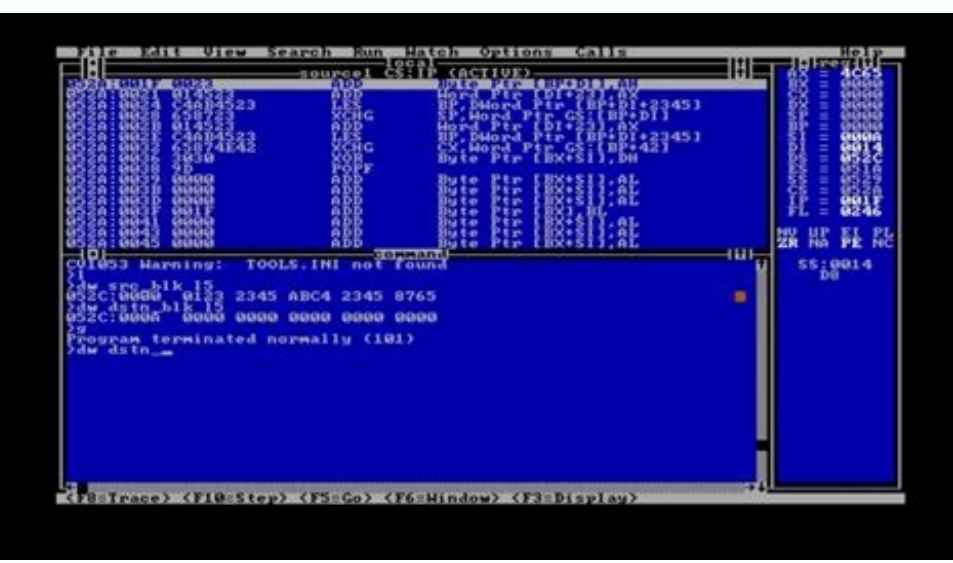


I'm not robot!

A	B	X
0	0	0
0	1	1
1	0	1
1	1	1



```

1 ; trouve n!
2 segment .text
3     global _fact
4     _fact:
5         enter 0,0
6
7         mov  eax, [ebp+8]    ; eax = n
8         cmp  eax, 1
9         jbe  term_cond     ; si n <= 1, terminé
10        dec  eax
11        push eax
12        call _fact         ; appel fact(n-1) récursivement
13        pop  ecx           ; réponse dans eax
14        mul  dword [ebp+8] ; edx:eax = eax * [ebp+8]
15        jmp  short end_fact
16 term_cond:
17        mov  eax, 1
18 end_fact:
19        leave
20        ret

```

FIG. 4.15 – Fonction factorielle récursive

LIZMAT PHARMACY
MAIN BRANCH
LOCATION :AWOSHIE
TEL :0201457854
TIN :
Vat Reg. No. :

DATE : 03-Jun-18 6:23:01 PM
CASHIER :

ITEM	QTY	PRICE	GH¢
MULTIVITAMIN	2	30.00	60.00
COLDAFEX	2	12.00	24.00
CYPROHEPTADINE	2	17.00	34.00
Sub Total:			118.00
Discount:			5.90
VAT @ 17.50%:			20.65
Bill Total:			132.75
Tendered:			135.00
Balance:			2.00

THANK YOU

Assembly language 8086 tutorial pdf. How to convert machine language to assembly language. 8086 assembly language tutorial ppt. 8086 assembly language programming tutorial pdf. Assembly language programming 8086 tutorial.

This tutorial is intended for those who are not familiar with assembler at all, or have a very distant idea about it. Of course if you have knowledge of some high level programming language (java, basic, c/c++, pascal...) that may help you a lot. but even if you are familiar with assembler, it is still a good idea to look through this document in order to understand anything, the simple computer model as i see it: the system bus (shown in yellow) connects the various components of a computer. The CPU is the heart of the computer, most of computations occur inside the CPU. RAM is a place to where the programs are loaded in order to be executed. Inside the CPU general purpose registers 8086 CPU has 8 general purpose registers, each register has its own name: AX - the accumulator register (divided into AH / AL). BX - the base address register (divided into BH / BL). CX - the count register (divided into CH / CL). DX - the data register (divided into DH / DL). SI - source index register. DI - destination index register. BP - base pointer. SP - stack pointer. despite the name of a register, it's the programmer who determines the usage for each general purpose register. the main purpose of a register is to keep a number (variable), the size of the above registers is 16 bit, it's something like: 0011000000111001b (in binary form), or 12345 in decimal (human) form. 4 general purpose registers (AX, BX, CX, DX) are made of two separate 8 bit registers, for example if AX= 0011000000111001b, then AH=00110000b and AL=00111001b. therefore, when you modify any of the 8 bit registers 16 bit register is also updated, and vice-versa. the same is for other 3 registers, "H" is for high and "L" is for low part. because registers are located inside the cpu, they are much faster than memory. accessing a memory location requires the use of a system bus, so it takes much longer. accessing data in a register usually takes no time. therefore, you should try to keep variables in the registers. register sets are very small and most registers have special purposes which limit their use as variables, but they are still an excellent place to store temporary data of calculations. segment registers CS - points at the segment containing the current program. DS - generally points at segment where variables are defined. ES - extra segment register, it's up to a coder to define its usage. SS - points at the segment containing the stack. although it is possible to store any data in the segment registers, this is never a good idea. the segment registers have a very special purpose - pointing at accessible blocks of memory. segment registers work together with general purpose register to access any memory value. For example if we would like to access memory at the physical address 12345h (hexadecimal), we would use the DS = 1230h and SI = 0045h. This is good, since this way we can access much more memory than with a single register that is limited to 16 bit values. CPU makes a calculation of physical address by multiplying the segment register by 10h and adding general purpose register to it (1230h * 10h + 45h = 12345h). the address formed with 2 registers is called an effective address. by default BX, SI and DI registers work with DS segment register; BP and SP work with SS segment register. Other general purpose registers cannot form an effective address! also, although BX can form an effective address, BH and BL cannot. special purpose registers IP - the instruction pointer. flags register - determines the current state of the microprocessor. IP register always works together with CS segment register and it points to currently executing instruction. flags register is modified automatically by CPU after mathematical operations, this allows to determine the type of the result, and to determine conditions to transfer control to other parts of the program. generally you cannot access these registers directly, the way you can access AX and other general registers, but it is possible to change values of system registers using some tricks that you will learn a little bit later. >>> Next Part >>> 8086 Assembler Tutorial for Beginners (Part 1) This tutorial is intended for those who are not familiar with assembler at all, or have a very distant idea about it. Of course if you have knowledge of some other programming language (Basic, C/C++, Pascal...) that may help you a lot. But even if you are familiar with assembler, it is still a good idea to look through this document in order to study emu8086 syntax. It is assumed that you have some knowledge about number representation (HEX/BIN), if not it is highly recommended to study Numbering Systems Tutorial before you proceed. What is an assembly language? Assembly language is a low level programming language. You need to get some knowledge about computer structure in order to understand anything. The simple computer model as I see it: The system bus (shown in yellow) connects the various components of a computer. The CPU is the heart of the computer, most of computations occur inside the CPU. RAM is a place to where the programs are loaded in order to be executed. Inside the CPU GENERAL PURPOSE REGISTERS 8086 CPU has 8 general purpose registers, each register has its own name: AX - the accumulator register (divided into AH / AL). BX - the base address register (divided into BH / BL). CX - the count register (divided into CH / CL). DX - the data register (divided into DH / DL). SI - source index register. DI - destination index register. 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Register sets are very small and most registers have special purposes which limit their use as variables, but they are still an

A special place to store temporary data of calculations. SEGMENT REGISTERS CS - points at the segment containing the current program. DS - extra register at segment where variables are defined. SS - points to a coder to the segment containing the stack. Although it is possible to store any data in the segment registers, this is never a good idea. The segment registers have a very special purpose - pointing at accessible blocks of memory. Segment registers work together with general purpose register to access any memory value. For example if we would like to access memory at the physical address 12345h (hexadecimal), we should set the DS = 1230h and SI = 0045h. This is good, since this way we can access much more memory than with a single register that is limited to 16 bit values. CPU makes a calculation of physical address by multiplying the segment register by 10h and adding general purpose register to it (1230h * 10h + 45h = 12345h). The address formed with 2 registers is called an effective address. By default BX, SI and DI registers work with DS segment register; BP and SP work with SS segment register. Other general purpose registers cannot form an effective address! Also, although BX can form an effective address, BH and BL cannot! SPECIAL PURPOSE REGISTERS IP - the instruction pointer. Flags Register - determines the current state of the processor. IP register always works together with CS segment register and it points to currently executing instruction. Flags Register is modified automatically by CPU after mathematical operations, this allows to determine the type of the result, and to determine conditions to transfer control to other parts of the program. Generally you cannot access these registers directly. >>> Next Part >>> Assembly level programming is very important to low-level embedded system design is used to access the processor instructions to manipulate hardware. It is a most primitive machine level language is used to make efficient code that consumes less number of clock cycles and takes less memory as compared to the high-level programming language. It is a complete hardware oriented programming language to write a program the programmer must be aware of embedded hardware. Here, we are providing basics of assembly level programming 8086. Assembly Level Programming 8086 Assembly Level Programming 8086 The assembly programming language is a low-level language which is developed by using mnemonics. The microcontroller or microprocessor can understand only the binary language like 0's or 1's therefore the assembler convert the assembly language to binary language and store it the memory to perform the tasks. Before writing the program the embedded designers must have sufficient knowledge on particular hardware or processor, so first we required to know hardware of 8086 processor. Hardware of The Processor 8086 Processor Architecture The 8086 is a processor that is represented for all peripheral devices such as serial bus, and RAM and ROM, I/O devices and so on which are all externally connected to CPU by using a system bus. The 8086 microprocessor has CISC based architecture, and it has peripherals like 32 I/O, Serial communication, memories and counters/timers. The microprocessor requires a program to perform the operations that require a memory for read and save the functions. 8086 Processor Architecture The assembly level programming 8086 is based on the memory registers. A Register is the main part of the microprocessors and controllers which are located in the memory that provides a faster way of collecting and storing the data. If we want to manipulate data to a processor or controller by performing multiplication, addition, etc., we cannot do that directly in the memory where need registers to process and to store the data. The 8086 microprocessor contains various kinds of registers that can be classified according to their instructions such as: General purpose registers: The 8086 CPU has consisted 8-general purpose registers and each register has its own name as shown in the figure such as AX, BX, CX, DX, SI,DI, BP, SP . These all are 16-bit registers where four registers are divided into two parts such as AX, BX, CX, and DX which is mainly used to keep the numbers. Special purpose registers: The 8086 CPU has consisted 2- special function registers such as IP and flag registers. The IP register point to the current executing instruction and always works to gather with the CS segment register. The main function of flag registers is to modify the CPU operations after mechanical functions are completed and we cannot access directly Segment registers: The 8086 CPU has consisted 4- segment registers such as CS, DS, ES, SS which is mainly used for possible to store any data in the segment registers and we can access a block of memory using segment registers. Simple Assembly Language Programs 8086 The assembly language programming 8086 has some rules such as The assembly level programming 8086 code must be written in upper case letters The labels must be followed by a colon, for example: label: All labels and symbols must begin with a letter All comments are typed in lower case The last line of the program must be ended with the END directive 8086 processors have two other instructions to access the data, such as WORD PTR - for word (two bytes), BYTE PTR - for byte. Op-Code and Operand Op-code: A single instruction is called as an op-code that can be executed by the CPU. Here the 'MOV' instruction is called as an op-code. Operands: A single piece data are called operands that can be operated by the op-code. Example, subtraction operation is performed by the operands that are subtracted by the operand. Syntax: SUB b, c 8086 microprocessor assembly language programs Write a Program For Read a Character From The Keyboard MOV ah, 1h //keyboard input subprogram INT 21h // character input // character stored in al MOV c, al //copy character from al to c

27/6/2022 · Emu8086 is a Microprocessor Emulator with integrated 8086 Assembler and Free Tutorial. Emulator runs on programs with a Virtual Machine. It emulates real hardware, such as screen, memory, and input/output devices. If the user has just begun to study assembly language, then this program is just for him. 8086 Processor Architecture. The assembly level programming 8086 is based on the memory registers. A Register is the main part of the microprocessors and controllers which are located in the memory that provides a faster way of collecting and storing the data. If we want to manipulate data to a processor or controller by performing multiplication, addition, etc., we cannot do that. ... 30/3/2021 · Soumitra Kumar Mandal, Microprocessor & Microcontroller Architecture, Programming & Interfacing using 8085,8086,8051, McGraw Hill Edu,2013. Yu-Cheng Liu and Glenn A.Gibson, "Microcomputer Systems: The 8086/8088 Family Architecture, Programming and Design", Second Edition, Prentice-Hall of India, 2007. Below is the full 8086/8088 instruction set of intel (81 instructions total). Most if not all of these instructions are available in 32-bit mode; they just operate on 32-bit registers (eax, ebx, etc.) and values instead of their 16-bit (ax, bx, etc.) counterparts.The updated instruction set is also grouped according to architecture (i386, i486, i686) and more generally is referred to as (32 ... 27/6/2022 · Emu8086 is a Microprocessor Emulator with integrated 8086 Assembler and Free Tutorial. Emulator runs on programs with a Virtual Machine. It emulates real hardware, such as screen, memory, and input/output devices. If the user has just begun to study assembly language, then this program is just for him. In depth tutorial on 8086 Microprocessor Architecture with details on functional unit, registers, ... 8086 microprocessor implements basic pipelining with the help of 6 bytes prefetch queue. ... bus and internal registers of 16 bits size. Thus, it can access and work on 16-bits data at a time. For writing code in assembly language, ... Below is the full 8086/8088 instruction set of Intel (81 instructions total). Most if not all of these instructions are available in 32-bit mode; they just operate on 32-bit registers (eax, ebx, etc.) and values instead of their 16-bit (ax, bx, etc.) counterparts.The updated instruction set is also grouped according to architecture (i386, i486, i686) and more generally is referred to as (32 ... Examples of such instructions are: ~~~~~ mov rax, [rbx + 8]; 8 is the displacement. mov rax, [rbx + rcx]; rbx is the base register. ~~~~~ There are hundreds of assembly directives and there's no hope of doing a comprehensive revision within the scope of a single tutorial, let alone a single section, even if we limit it to only the most common assembly directives. 16/2/2020 · 3. Setup the Build Customization as Assembly Language. Right click at the project name > Build Dependencies > Build Customizations... Select the "masm", then OK. Right click at the project name > properties > Linker > System > SubSystem : Windows This will prevent the assembly program to show the console window (the black text-mode window). 20/4/2015 · The 8086 microprocessor is one of the family of 8086, 80286, 80386, 80486, Pentium, Pentium I, ... We use __asm__ keyword to embed assembly language statements into a C program. ... The tutorial provides three other c source codes that do simpler things such as print "X" and "hello world" and I had no problem running them. El lenguaje ensamblador o assembly (en inglés: assembly language y la abreviación asm) es un lenguaje de programación de bajo nivel.Consiste en un conjunto de mnemónicos que representan instrucciones básicas para los computadores, microprocesadores, microcontroladores y otros circuitos integrados programables. Implementa una representación simbólica de los códigos. ... 25/9/2014 · JUNE JULY 2013 VTU MICROCONTROLLERS 4th SEM B.E. Degree Examination Part - A Question 1 a. Compare the CPU architectures: i. CISC and RISC. CISC: CISC: Complex Instruction Set Computer In the early days of the computer development work was done in assembly language because high level languages were not developed. Examples of such instructions are: ~~~~~ mov rax, [rbx + 8]; 8 is the displacement. mov rax, [rbx + rcx]; rbx is the base register. ~~~~~ There are hundreds of assembly directives and there's no hope of doing a comprehensive revision within the scope of a single tutorial, let alone a single section, even if we limit it to only the most common assembly directives. Below is the full 8086/8088 instruction set of Intel (81 instructions total). Most if not all of these instructions are available in 32-bit mode; they just operate on 32-bit registers (eax, ebx, etc.) and values instead of their 16-bit (ax, bx, etc.) counterparts.The updated instruction set is also grouped according to architecture (i386, i486, i686) and more generally is referred to as (32 ... In this x86-64 computer architecture, long mode is the mode where a 64-bit operating system can access 64-bit instructions and registers. 64-bit programs are run in a sub-mode called 64-bit mode, while 32-bit programs and 16-bit protected mode programs are executed in a sub-mode called compatibility mode. Real mode or virtual 8086 mode programs cannot be natively run in ... 22/10/2021 · In this tutorial, we will see different integer division instructions supported by 8086 microprocessors. We will also provide assembly program examples of each divide instruction. In this series of 8086 microprocessor tutorials, we previously discussed; 8086 Microprocessor Addressing Modes , 8086 Data Transfer Instructions , 8086 Integer Arithmetic Instructions , ... 22/5/2018 · 8086 program to sort an integer array in ascending order: 8086 program to sort an integer array in descending order: 8086 program to find the min value in a given array: 8086 program to determine largest number in an array of n numbers: Assembly language program to find largest number in an array: Convert C/C++ code to assembly language ... 30/3/2021 · Soumitra Kumar Mandal, Microprocessor & Microcontroller Architecture, Programming & Interfacing using 8085,8086,8051, McGraw Hill Edu,2013. Yu-Cheng Liu and Glenn A.Gibson, "Microcomputer Systems: The 8086/8088 Family Architecture, Programming and Design", Second Edition, Prentice-Hall of India, 2007. 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Examples of high-level language are python,c#, etc. Difference between assembly language and high-level language : In depth tutorial on 8086 Microprocessor Architecture with details on functional unit, registers, ... 8086 microprocessor implements basic pipelining with the help of 6 bytes prefetch queue. ... bus and internal registers of 16 bits size. Thus, it can access and work on 16-bits data at a time. For writing code in assembly language, ... Below is the full 8086/8088 instruction set of Intel (81 instructions total). Most if not all of these instructions are available in 32-bit mode; they just operate on 32-bit registers (eax, ebx, etc.) and values instead of their 16-bit (ax, bx, etc.) counterparts.The updated instruction set is also grouped according to architecture (i386, i486, i686) and more generally is referred to as (32 ... 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